



Tech Info Library

High Speed Serial Interface: Manual Errata (11/96)

Revised: 11/21/96
Security: Everyone

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Article Created: 21 September 1984
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TOPIC -----

Listed below are corrections to the High Speed Serial Interface manual
(030-0012-00).

DISCUSSION -----

Corrections
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Page 16

The information for switch 4 is reversed, it should read:

ON = delay enabled
OFF = delay disabled

Page 32

Two labels on the schematic diagram are wrong:

| | | |
|----------|-----------|----------|
| XMIT | | XMIT |
| -----O B | should be | -----O C |
| WIRE | | WIRE |
| TO | | TO |
| PIN 3 | | PIN 3 |

| | | |
|----------|-----------|----------|
| RECV | | RECV |
| -----O C | should be | -----O B |
| WIRE | | WIRE |
| TO | | TO |

PIN 2

PIN 2

P8A PROM

The P8A PROM interferes with the operation monitor routines that use \$3C. The P8A uses this location as a temporary storage space and does not restore it.

Article Change History:

19 Nov 1996 - Reviewed for technical accuracy, revised formatting.

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