



Tech Info Library

Apple III Univ Parallel Interface Card: Specs (Discontinued)

Revised: 9/13/93
Security: Everyone

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Data Lines

- 16 output (24 mA at 0.5 V)
- 8 input (1 LSTTL load)

Control Lines

- Data Ready Output
- Acknowledge Input
- Reset Data Ready
- Output Strobe
- Signal Ground
- Chassis Ground

Control Signal Polarity

Software-settable

Signal Levels

LSTTL

Strobe Length

1-15K sec. in 2&5

Interrupts

Software controlled

Connector

- 40-Pin Ribbon (parallel), center-keyed
- Accepts 20-Pin Ribbon (Printer), center-keyed

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