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Macintosh IIVx: Control Chips (7/93)

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Security: Everyone

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TOPIC -----

This article describes the Macintosh IIVx (Performa 600) control chips.

DISCUSSION -----

VASP

The VASP controls all timing, video generation, memory mapping, sound, and clock generation. VASP is a modification of the V8 ASIC used in the Macintosh LC and Macintosh LC II.

Ariel

Ariel is the 256x24 bit color look-up table (CLUT) for Macintosh IIVx.

CoMBo Chip (85C80)

The CoMBo chip provides identical serial and SCSI interface control for Macintosh IIVx. Macintosh IIVx provides on-board SCSI termination when no external SCSI device is connected to the DB-25 connector.

Egret (68HC05)

The Egret chip provides keyboard scanning, real-time clock, ADB interface, parameter RAM storage, power on reset, and power supply control.

Article Change History:
07/12/93 - Revised to include Performa 600.

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